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From: Robert D. Marshall, Jr.
Texas Instruments Incorporated
Facsimile: 972-917-4418
Phone: 972-917-5290

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-22423

Venkat V. Easwar

Art Unit: 2624

Serial No.: 09/713,446

Examiner: Kyle M. Pendergrass


Filed: November 15, 2000

Conf. No.: 3256

For: Multi-Level Dither Screening on a Split Arithmetic Logic Unit Processor

CERTIFICATION OF FACSIMILE TRANSMISSION

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Robin E. Barnum

October 15, 2004
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FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT 111 (18 Pages)
<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT (# Page)
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<input type="checkbox"/> CONTINUATION APPN (# Pages)	
<input type="checkbox"/> DIVISIONAL APPN	
NAME OF INVENTOR(S): Venkat V. Easwar	
TITLE OF INVENTION: Multi-Level Dither Screening on a Split Arithmetic Logic Unit Processor	
TI FILE NO.: TI-22423	DEPOSIT ACCT. NO.: 20-0668
FAXED: 10/15/04	RECEIPT DATE & SERIAL NO.: Serial No.: 09/713,446 Filing Date: November 15, 2000
DUE: 10/16/04	
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Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75265

OCT 15 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Easwar

Art Unit: 2624

Serial No.: 09/713,446

Examiner: Kyle M. Pendergrass

Filed: November 15, 2000

Docket: TI-22423

For: MULTI-LEVEL DITHER SCREENING ON A SPLIT ARITHMETIC LOGIC UNIT
PROCESSOR

AMENDMENT UNDER 37 C.F.R. 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATION OF FAX TRANSMITTAL
UNDER 37 C.F.R. §1.6(b)

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Robin E. Barnum

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated July 16, 2004. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: